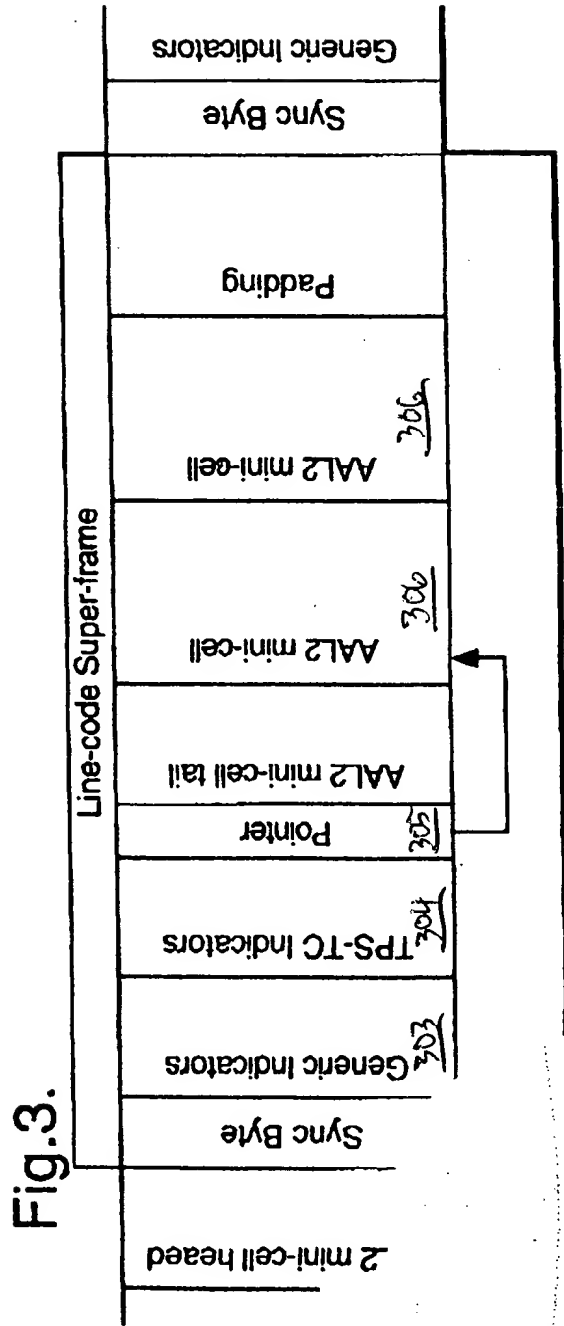
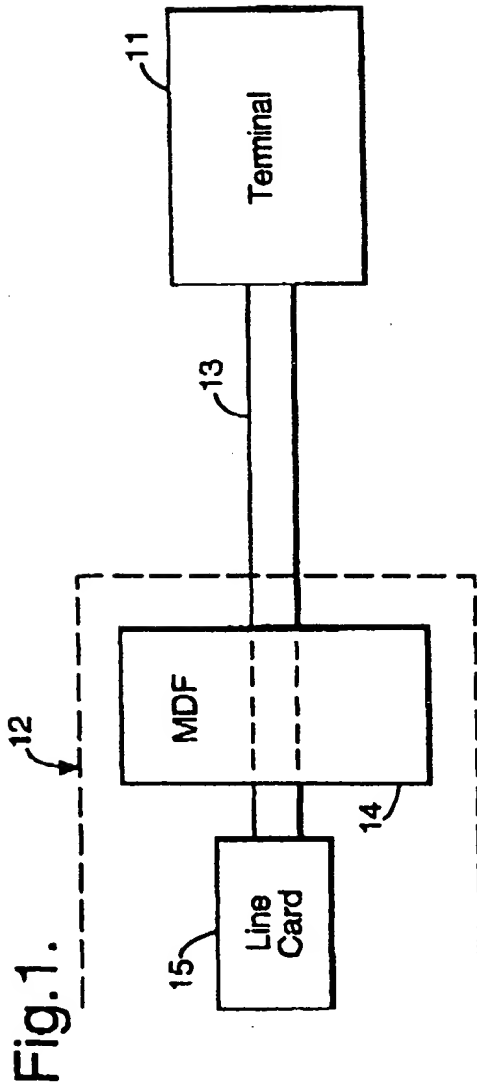


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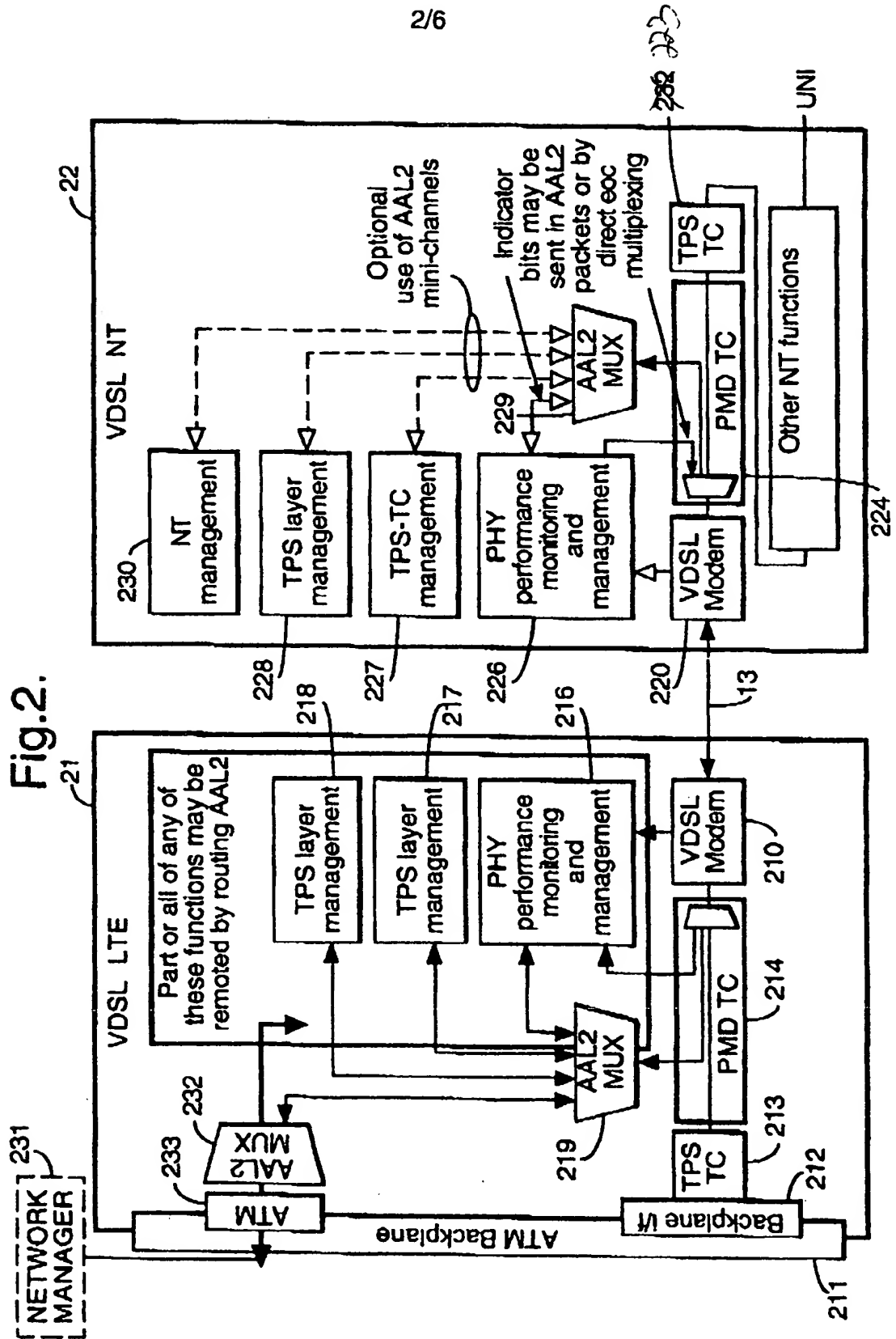
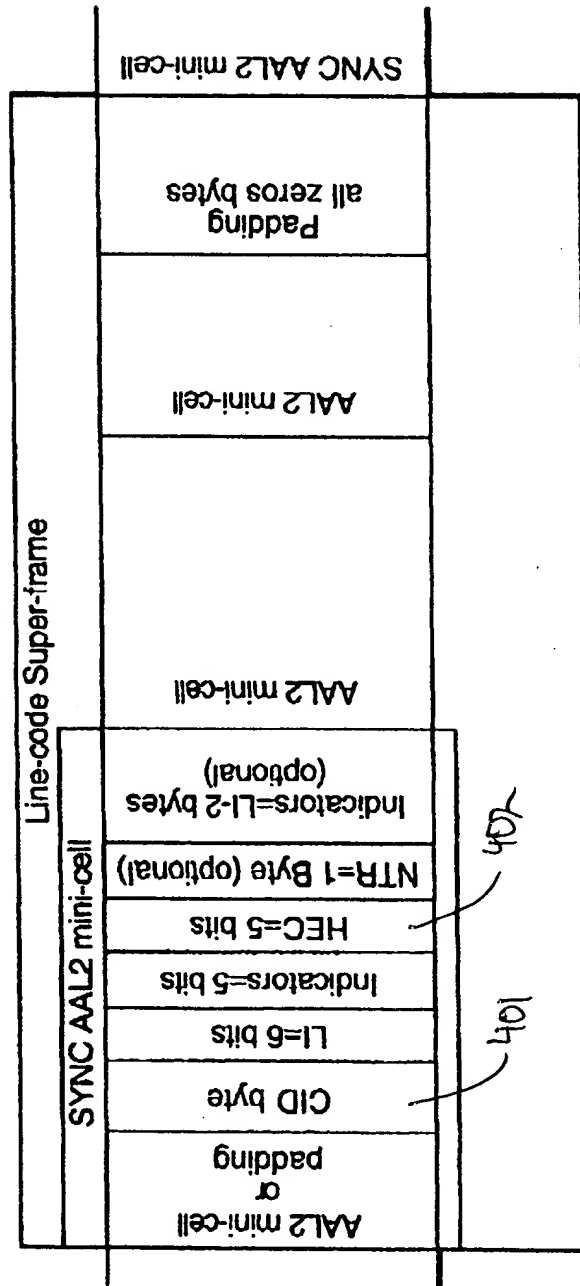
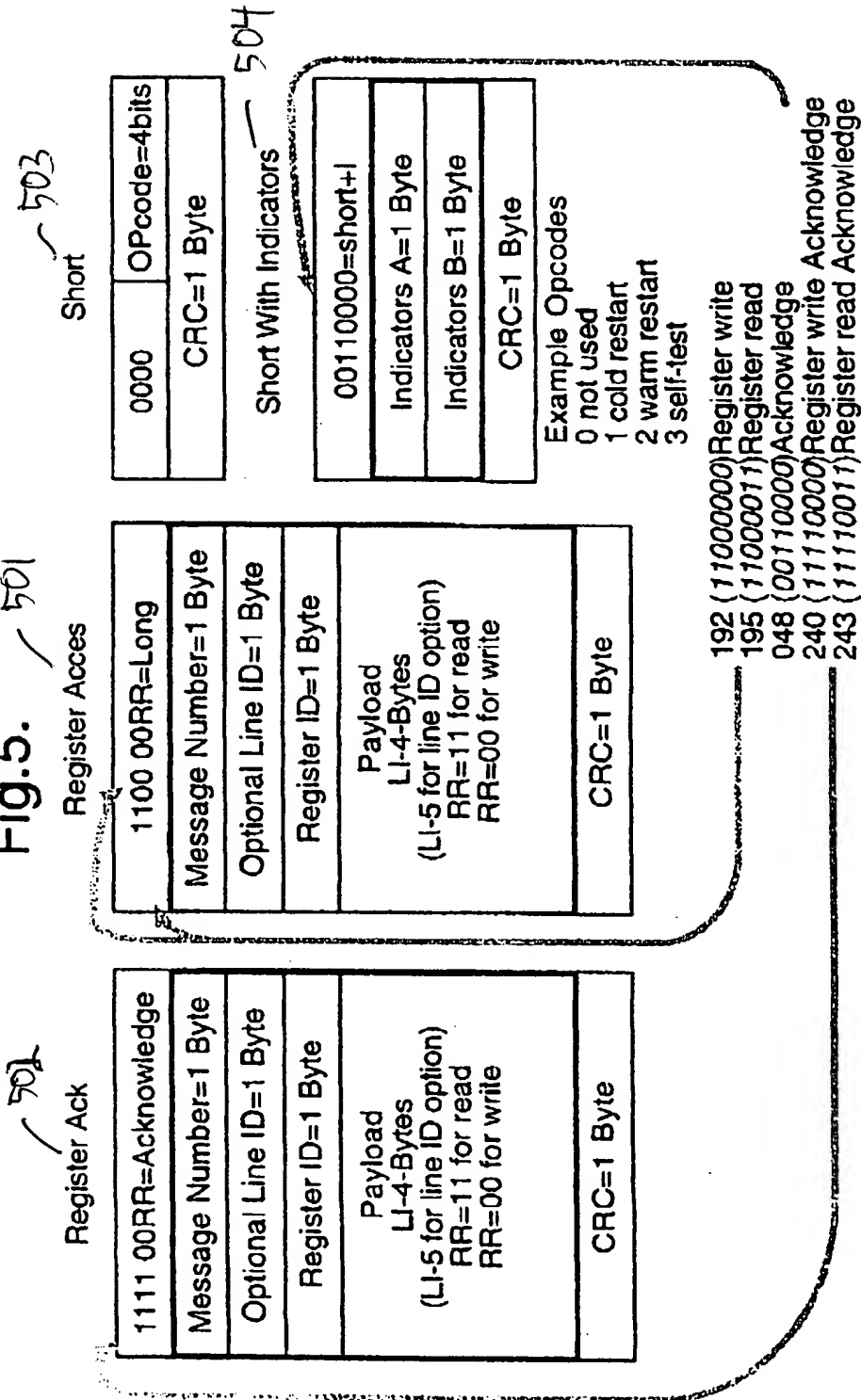


Fig.4.



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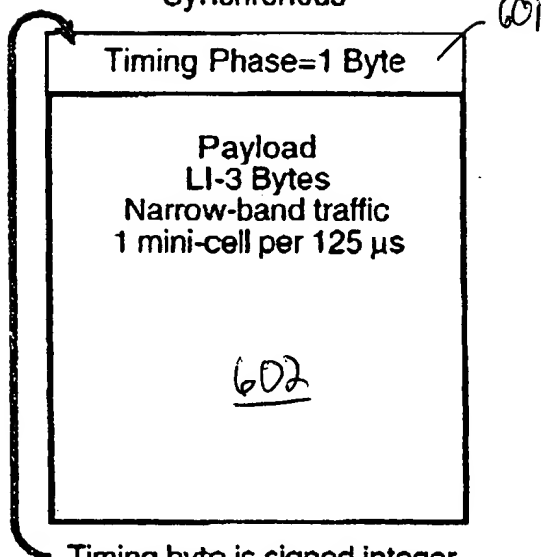
Fig.5. 501



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Fig.6a.

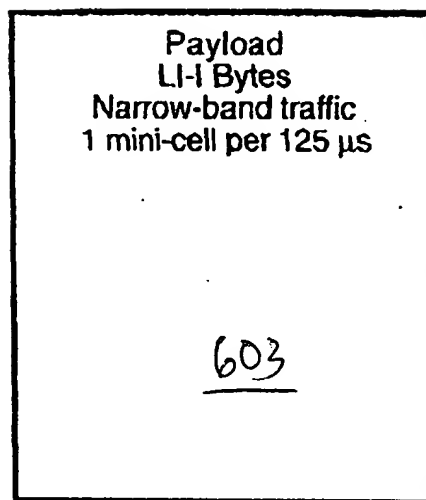
Synchronous



Timing byte is signed integer  
giving offset from NTR phase

Fig.6b.

Pleisochronous



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